

Applu No. 10/727,158  
Amdt. Dated June 1, 2004  
Response to Notice to File Corrected Application Papers of April 15, 2004

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**Amendments to the Specification:**

The paragraph beginning at Page 10, please insert new line 30 as follows:

**Figure 63A Timers sub-block partition diagram**

The paragraph beginning at Page 10, please insert new line 32 as follows:

**Figure 66 is a schematic of a timing pulse generator**

The paragraph beginning at Page 16, line 5, to be amended as follows:

**Figure 278 is intentionally omitted shows power on reset**

The paragraph beginning at Page 18, line 39 to be amended as follows:

**Figure 388A shows setup for multiple preauth fields**

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**Amendments to the Drawings:**

Please delete Figure 413 on page 331 of the drawings and insert Figure 278 on page 237 of the drawings.